[LIQUID CRYSTAL DISPLAY DRIVING CIR-CUIT, VERIFYING APPARATUS AND ER-ROR TOLERANCE METHOD THEREOF]

Abstract

A liquid crystal display driving circuit, verifying apparatus and error tolerance method is disclosed. The liquid crystal display driving circuit has a plurality of driving stages each having a plurality of verifying apparatus, a logic operation unit and a driving switch. Each verifying apparatus comprises a storage unit, a data switch and an edge detector. The storage unit receives a first and a second trigger pulse during a first and a second time period and then outputs a first and a second shifted signal that correspond to the first and the second triggered pulse submitted to the storage unit. The first and the second shifted signal are transferred to a first and a second output path through switching the data switch during the first and the second time period. The edge detector receives the first shifted signal and set the second output path to a pre-defined logic potential during the second time period if no edge transition is detected during the first time period. The logic operation unit receives the data on the second output path and

executes a corresponding logic operation according the pre-defined logic potential so that the output from the logic operation unit is unaffected by the pre-defined logic potential. The driving switch cuts off the pixel circuit from the driving stage during the first time period but reconnects with the driving stage during the second time period.